

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasuhiko KASAMA et al.

Conf. 9839

Application No. 10/577,526

Group 2876

Filed April 28, 2006

Examiner

COMPLEMENTARY MISFET AND INTEGRATED CIRCUIT

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 16, 2007

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks begin on page 6 of this paper.